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In the present invention, a first program to transmit data stores the data to the main storage area in the desired time interval. A second program to receive data refers to the above area using RDMA in the desired time interval. Otherwise, the second program to receive data refers to the main storage in the desired time interval. A first program to transmit data stores data to above area using RDMA in the desired time interval. Moreover, in order to detect the passing between the write and read operations, an identifier is provided to each record and the access sequence of the identifier and record is inverted in the read and write operations. Or, an identifier and an error checking code are added to each record.